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Editorial

We publish the first issue of the seventh volume of the **Electronic Devices** with the below described research.

Authors in the paper on “**DynMapNoCSIM: A Dynamic Mapping SIMULATOR for Network on Chip based MPSoC**” have proposed a simulator for Network-on-Chip based Multi-Processor Systems-on-Chip. The proposed JAVA based Dynamic Mapping simulator for NoC-based MPSoC architecture is built on object-oriented modular design of the NoC-based MPSoC architecture components. They have validated the outputs of DynMapNoCSIM with the studies of existing and proposed Dynamic mapping strategies.

Farhaad Raad in his paper on “**Optimal Rate Control in Wireless Network on Chip (WiNoC)**” has studied the wireless NoC (WiNoC) architectures. Using nonlinear optimization theory and a synchronous iterative algorithm the rate control problem in the WiNoC has been investigated. This issue is solved based on the gradient projection method.

Noaman Abduljabbar Ramadhan, Rahiman and Abdullah Zuriati in their paper on “**Performance Evaluation of an Efficient Scheduling Algorithm For Surveillance Monitoring System Using WiMAX**” used video, audio and text data for analyzing which one can efficiently support these three scheduling algorithms between (TE and PQ) queue scheduling. The simulation results prove that the proposed WIMAX based video surveillance system improved its video transmission rate and improved packet delivery ratio.

We will come up with more research in the next issue.

Editors