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Editorial

We present the last issue of the **Journal of Electronic Systems** with the below mentioned papers.

In the first paper on "**UHF range and B/G test channel for channel converters**", the authors have studied the oscillation frequencies in the modulators and channel converters in a Head End for cable TV. They have used the channel 46 from the UHF range of B/G and the test channel is (n).

In the second paper on "**Polynomial functions using transistor noise parameters in a noise-wave model**", The authors have studied the similarity between the noise wave parameters and the noise parameters. They extracted the optimization procedures in circuit simulators using the device intrinsic circuit and available measured transistor noise parameters are related to the whole device. Further they have used the transistor noise parameters that studied the measures.

In the third paper on "**Use of standard two phase dc converter architecture for ZVS technique**", the authors have used the CMOS technology with Cadence to generate a two-phase switching mode for the dc converter with zero voltage switching. The authors have found that the effectiveness of the system increases marginally when the help of the standard two-phase dc converter architecture where we have deployed the ZVS technique.

The published papers of this issue mark enhanced technical features.

Editors