Editorial

We bring the first issue of the fifteenth volume of the **Journal of Electronic Systems** with the below papers.

In the first paper, "**Analysis of Hardware Monitors in a Multicore Environment: Experience from Implementation,**" the authors viewed the key issue as software timing analysis and analysis of Hardware Monitors. Now, they developed the hardware profiling challenge from an industrial viewpoint and tackled methodological and practical issues related to monitoring a multicore application. They demonstrated how the profiling tools can be utilised alongside custom benchmarks.

In the following paper, "**A Propositional SAT Encoding in Quantum Computing**," the authors advocated a propositional SAT encoding that can theoretically be applied to any quantum circuit. They proved that constructing such an encoding is generally impractical due to the inherent complexity of representing quantum states. They highlighted how the proposed encoding can be utilised for the class of Clifford circuits. Lastly, we provide empirical evidence of the applicability and effectiveness of the proposed encoding for Clifford circuits.

In the last paper, "**The Portfolio Examination of Recent SAT Solvers and Prediction Models**," the authors studied the portfolios consisting of recent SAT solvers. They identified optimal portfolios using both exact and approximate methods and analysed how the size of the portfolio, denoted as k, influences performance. They found that the portfolios generated through approximate methods perform comparably to the optimal solutions in practice.

We hope these papers are interesting to read.

Editors