

Contents

Editorial i

Research

Automatic Terminal Information System for El Alto Airport -
N. I. Vargas-Cuentas, A. Román-González 73

System-Level Simulation and Radio Resource Management for Distributed Antenna
Systems with Cognitive Radio and Multi-Cell Cooperation using Imperfect Information -
Ramiro Sámano-Robles, Atilio Gameiro, Nuno Pereira, Eduardo Tovar 84

A Novel FPGA Architecture using Memristor-Transistor Hybrid Approach-
M.Hassan Aslam, Umer Farooq 101

Book Review 117

Conference Notification 118

- First International Conference on Real Time Intelligent Systems (RTIS 2016)
Taiyuan, China
- The Seventh International Conference on the Applications of Digital Information and Web
Technologies (ICADIWT 2016)

Editorial

Automatic Terminal Information System installed in airport has a few problems such as congestion in communication frequency, weather report generation and so on. The authors Vargas-Cuentas and Román-González in their paper on “**Automatic Terminal Information System for El Alto Airport**” have introduced and implemented the ATIS (Automatic Terminal Information System) for El Alto airport in Peru.

In the next paper on “**System-Level Simulation and Radio Resource Management for Distributed Antenna Systems with Cognitive Radio and Multi-Cell Cooperation using Imperfect Information**” the authors *Ramiro Sámano-Robles, Atílio Gameiro, Nuno Pereira* and *Eduardo Tovar* proposed a system-level simulator (SLS) to solve the problem in the design of billing/licensing schemes in distributed antenna systems. They have proposed an advanced RRM solution for a multi-cell DAS in a dense urban Manhattan scenario with two levels of cooperation.

Hassan Aslam and *Umer Farooq* in the last paper on “**A Novel FPGA Architecture using Memristor-Transistor Hybrid Approach**” proposed design of a novel FPGA architecture based on memristor-transistor hybrid approach. They proposed hybrid basic building blocks of FPGA which are smaller in size and lower in power consumption as compared to the conventional transistor-only building blocks.

The papers in this issue are technically elegant.

Editors