Journal of Multimedia Processing and Technologies Volume 4 Number 4 December 2013

Contents	
Editorial	i
Research	
Towards a File System Optimized for Scalable Media- Heiko Sparenberg, Volker Bruns, Seigfried Foessel	195
An Approach Making the Shape Matching Techniques Robust to Noise- Sadaf Sajjad, Shehzad Khalid	202
Face Detection Using Template Face Mask- Serdar Yilmaz, Metin Salhmuhsn	211
FPGA Implementation and Simulation of Guided Image Filtering Rajapriya, S, Rijuvana Begum, A, Kannan, G	218
Book Review	228
Conference Notification	229
Ninth International Conference on Digital Information Management (ICDIM 2014)	
 Fourth International Conference on Innovative Computing Technology 	

Editorial

This issue marks the publication of the fourth volume of the Journal of Multimedia Processing Technologies wherein we publish a few quite interesting papers.

In the first paper on **"Towards a File System Optimized for Scalable Media"** the authors *Heiko Sparenberg, Volker Bruns* and *Seigfried Foessel* have addressed the file system in the scalable media. Basically they have proposed Use-Case-Optimized DAta Storage (UCODAS) a progressive file storage system to overcome the shortcoming of HDD. With experiments they have shown that the UCODAS can increase the data-throughput of a conventional HDD.

In the second paper, the authors *Sadaf Sajjad* and *Shehzad Khalid* have addressed the shape matching issue. Existing space descriptors fail to address the small cracks or slits present in the contour of the images. The authors presented a pre-processing approach to identify and remove small cracks and slits in the shape contour. They tested the effectiveness of proposed pre-processing approach using publicly available shape datasets.

To get face image from entrance pictures, *Serdar Yilmaz* and *Metin Salhmuhsn* have developed a template matching method. They performed difference matrix methods for face detection-face identification. Further they have codified the application in Artificial Intelligence.

In the last paper on **"FPGA Implementation and Simulation of Guided Image Filtering"** the authors *Rajapriya, Rijuvana Begum,* and *Kannan* have secured an implementation guided image filtering using a FPGA Xilinx Spartan 3E, for educational purposes.

The papers published in this issue mark significance and elegance on varied themes of the Multimedia processing and technologies.

Editors

i