

Editorial

We have released this issue of the Progress in Signals and Telecommunication Systems with the below research.

In the first paper on “**Enhancing the efficiency of fixed transmitters for better communication**”, the authors have identified the correct location of the receivers to improve the fixed transmitter functioning. They have applied the line rasterization method for producing terrain profile and to avoid arctangent function for angle calculation. This system ensures to understand the grid resolution and improve the accuracy and effectiveness. The proposed model allows to understand the grid resolution and improve the accuracy and effectiveness.

In the second paper on “**Models for digital program amplifiers**”, the authors introduced the models for digital program amplifiers. Using microchip, the authors extracted the measures with one and two channels PGA. With the help of the structure and operation principles further they developed the circuits. The testing outcome confirm with the theoretical propositions.

In the final paper on “**Bivalent logic in Xfault simulators**” the authors developed the concurrent X fault simulator in this work. The main problem is the use of bivalent logic for bad gates. The authors fixed this issue by the use of trivalent logic and bit presentation.

Finally, they solved the issue of Xfault source line presentation with the use of binary arrays for source lines description.

We will come out with more research in the next issue.

Editors