Editorial

We bring the first issue of this volume of the Digital Signal Processing with the below papers.

In the opening paper, "**The design of high-sensitivity capacitive sensors**," the authors proposed a design based on the destabilizing effect of temperature and supply voltage. They have studied the stability and sensitivity of several kinds of stabilized quartz generators. The developed sensors are used in wireless sensor networks.

In the next paper, "**Experiment-based Multicore Architecture**," the authors describe approaches for multicore WCET analysis and present some strategies to reduce resource conflicts. They introduce two multicore architectures and give some hints concerning their suitability for WCET analysis.

In the last paper, "**Parallel Software Framework for Time-Critical Core Systems**," the authors proposed a design based on the destabilizing effect of temperature and supply voltage. They have deployed the developed sensors in wireless sensor networks. The generated sensors yield productive results in experimentation.

We will bring more research into the forthcoming issues.

Editors